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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: T. ARAI, et al.
Application No.: 10/085,063
Filed: March 1, 2002
For: FABRICATION METHOD OF SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE
Group: 1765
Examiner: B. X. Tran
Conf. No.: 9140

AMENDMENT

Mail Stop: Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

June 27, 2005

Sir:

In response to the Office Action mailed March 25, 2005, please amend the above-identified application as listed in the following, and as set forth on the following pages:

AMENDMENTS TO THE CLAIMS; and

REMARKS are included following the Amendments.